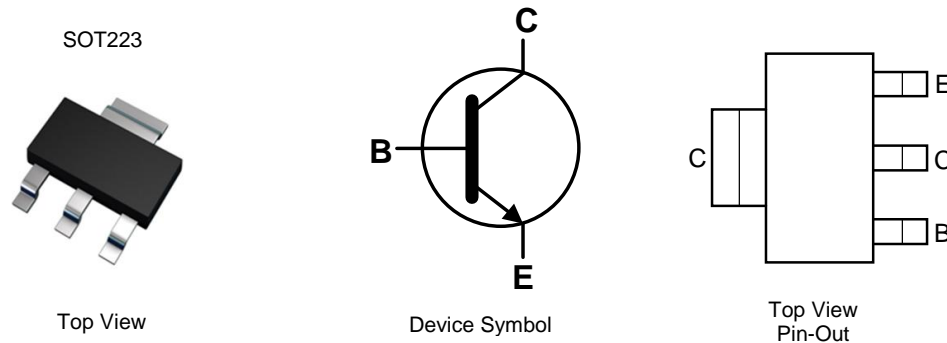


Features

- $BV_{CEO} > 100V$
- $I_C = 2A$ High Continuous Current
- $I_{CM} = 6A$ Peak Pulse Current
- Low Saturation Voltage $V_{CE(SAT)} < 300mV @ 1A$
- Complementary PNP Type: FZT753
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic. "Green" Molding Compound; UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208③
- Weight: 0.112 grams (Approximate)

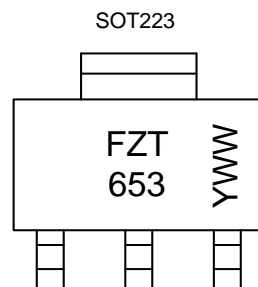


Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Peel
FZT653TA	AEC-Q101	FZT653	7	12	1,000
FZT653QTA	Automotive	FZT653	7	12	1,000
FZT653TC	AEC-Q101	FZT653	13	12	4,000

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/product_compliance_definitions.html.
 5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



FZT 653 = Product Type Marking Code
 YWW = Date Code Marking
 Y or \bar{Y} = Last Digit of Year (ex: 6 = 2016)
 WW or $\bar{W}W$ = Week Code (01 to 53)

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CB0}	120	V
Collector-Emitter Voltage	V _{CEO}	100	V
Emitter-Base Voltage	V _{EBO}	7	V
Continuous Collector Current	I _C	2	A
Peak Pulse Current	I _{CM}	6	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

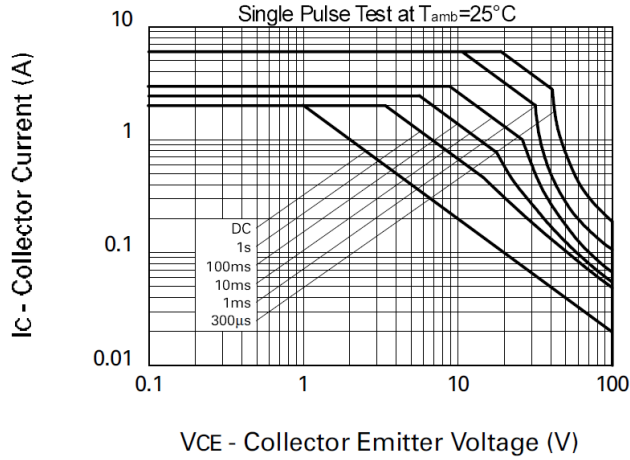
Characteristic	Symbol	Value	Unit
Power Dissipation	P _D	(Note 6)	3
		(Note 7)	2
		(Note 8)	1.6
		(Note 9)	1.2
Thermal Resistance, Junction to Ambient	R _{θJA}	(Note 6)	41.7
		(Note 7)	62.5
		(Note 8)	78.1
		(Note 9)	104
Thermal Resistance Junction to Lead	R _{θJL}	12.9	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

ESD Ratings (Note 11)

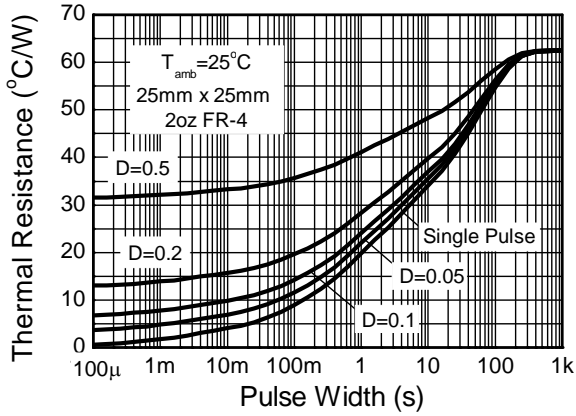
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	C

- Notes:
6. For a device mounted with the collector lead on 50mm x 50mm 2oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in a steady-state.
 7. Same as Note 6, except the device is mounted on 25mm x 25mm 2oz copper.
 8. Same as Note 6, except the device is mounted on 25mm x 25mm 1oz copper.
 9. Same as Note 6, except the device is mounted on minimum recommended pad layout.
 10. Thermal resistance from junction to solder-point (at the end of the collector lead).
 11. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

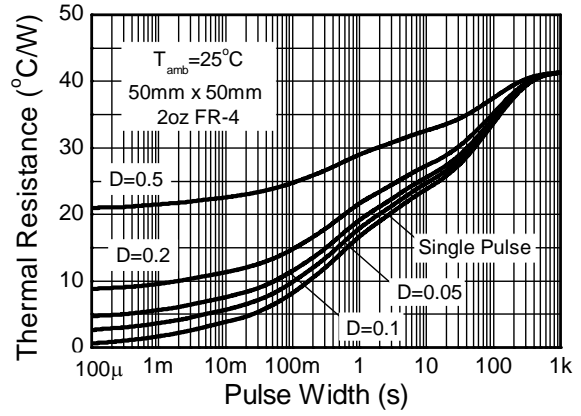
Thermal Characteristics and Derating Information



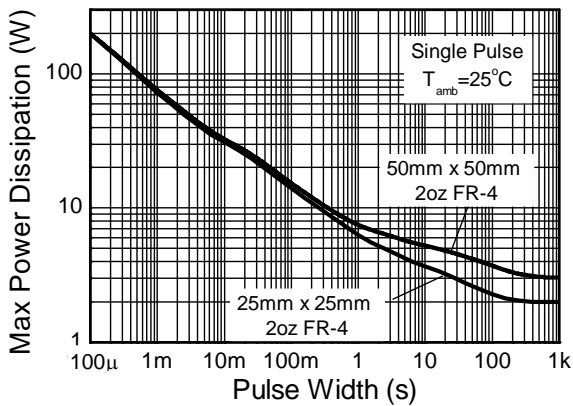
Safe Operating Area



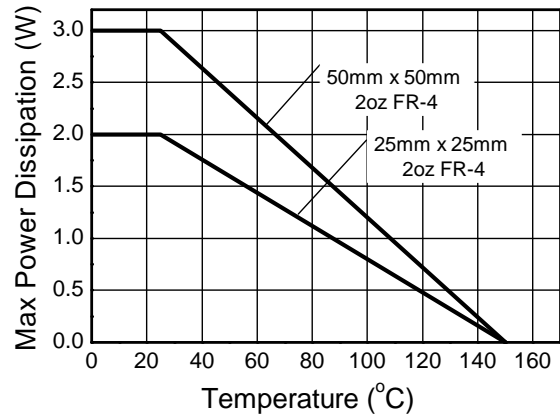
Transient Thermal Impedance



Transient Thermal Impedance



Pulse Power Dissipation



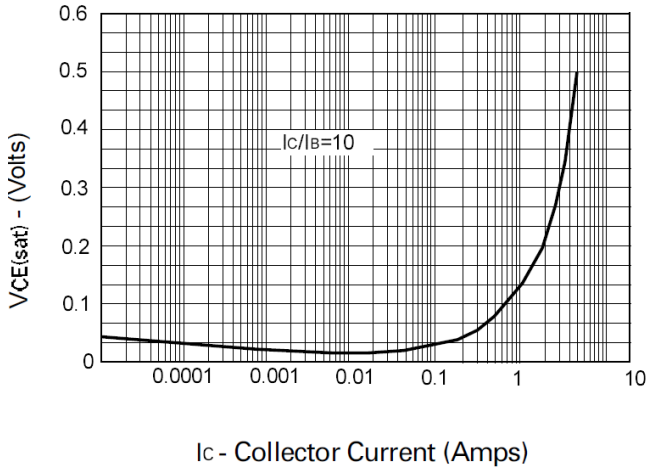
Derating Curve

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

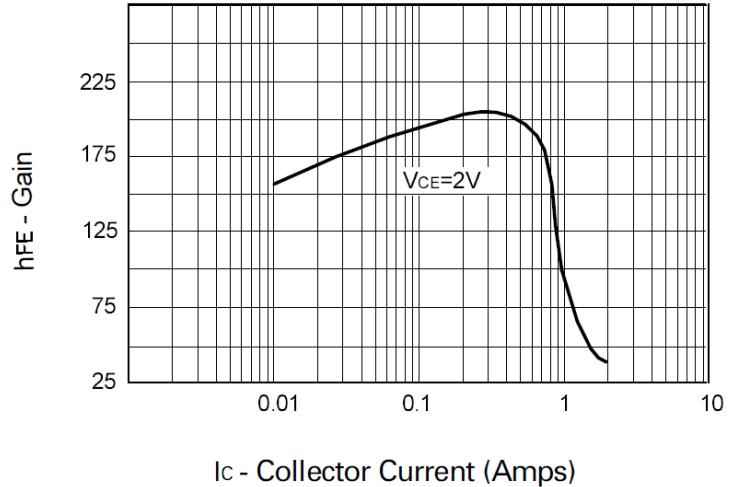
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV_{CBO}	120	–	–	V	$I_C = 100\mu\text{A}$
Collector-Emitter Breakdown Voltage (Note 12)	BV_{CEO}	100	–	–	V	$I_C = 10\text{mA}$
Emitter-Base Breakdown Voltage	BV_{EBO}	7	–	–	V	$I_E = 100\mu\text{A}$
Collector Cut-Off Current	I_{CBO}	–	< 1	100	nA	$V_{CB} = 100\text{V}$
		–	–	10	μA	$V_{CB} = 100\text{V}, T_A = +125^\circ\text{C}$
Emitter Cut-Off Current	I_{EBO}	–	< 1	100	nA	$V_{EB} = 5.6\text{V}$
Collector-Emitter Saturation Voltage (Note 12)	$V_{CE(SAT)}$	–	0.13	0.3	V	$I_C = 1\text{A}, I_B = 100\text{mA}$
		–	0.23	0.5		$I_C = 2\text{A}, I_B = 200\text{mA}$
Base-Emitter Saturation Voltage (Note 12)	$V_{BE(SAT)}$	–	0.9	1.25	V	$I_C = 1\text{A}, I_B = 100\text{mA}$
Base-Emitter Turn-On Voltage (Note 12)	$V_{BE(ON)}$	–	0.8	1.0	V	$I_C = 1\text{A}, V_{CE} = 2\text{V}$
DC Current Gain (Note 12)	h_{FE}	70	200	–	–	$I_C = 50\text{mA}, V_{CE} = 2\text{V}$
		100	200	300		$I_C = 500\text{mA}, V_{CE} = 2\text{V}$
		55	110	–		$I_C = 1\text{A}, V_{CE} = 2\text{V}$
		25	55	–		$I_C = 2\text{A}, V_{CE} = 2\text{V}$
Current Gain-Bandwidth Product	f_T	140	175	–	MHz	$V_{CE} = 5\text{V}, I_C = 100\text{mA}, f = 100\text{MHz}$
Switching Times	t_{ON}	–	80	–	ns	$I_C = 500\text{mA}, V_{CC} = 10\text{V}, I_{B1} = -I_{B2} = 50\text{mA}$
	t_{OFF}	–	1200	–		
Output Capacitance	C_{OBO}	–	–	30	pF	$V_{CB} = 10\text{V}, f = 1\text{MHz}$

Note: 12. Measured under pulsed conditions. Pulse width $\leq 300\mu\text{s}$. Duty cycle $\leq 2\%$.

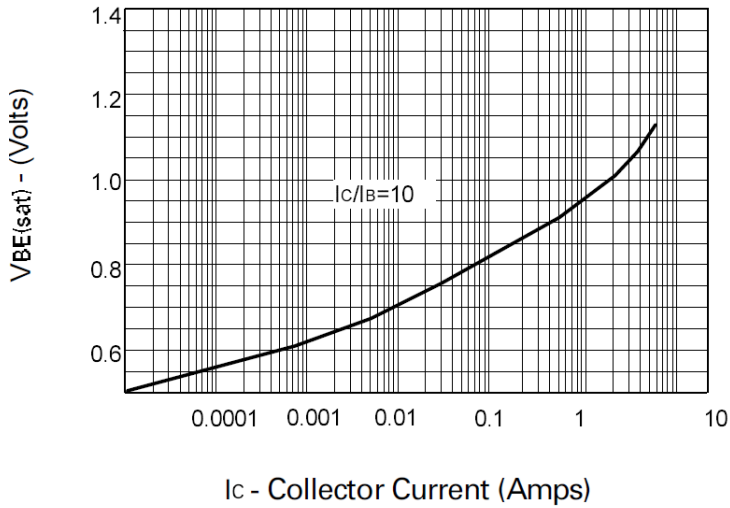
Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)



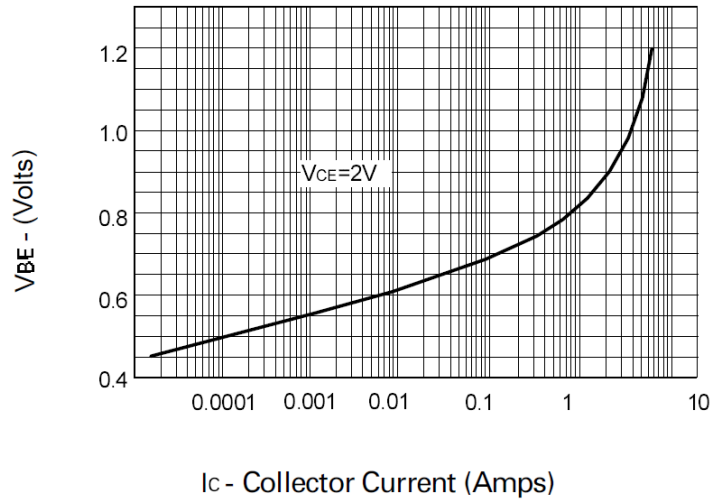
$V_{CE(sat)}$ v I_C



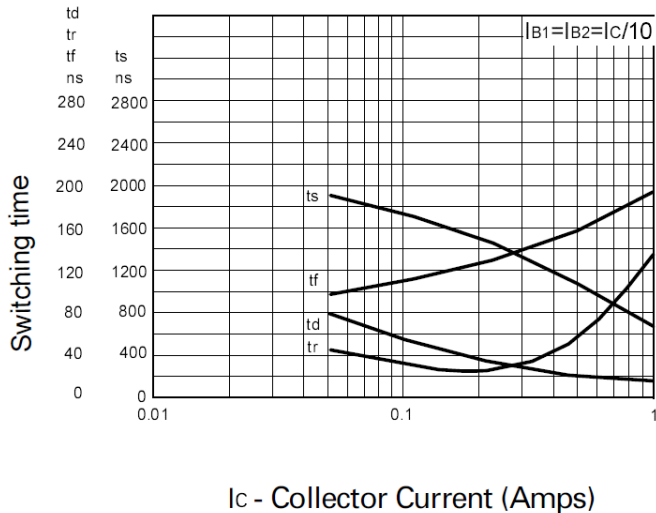
h_{FE} v I_C



$V_{BE(sat)}$ v I_C



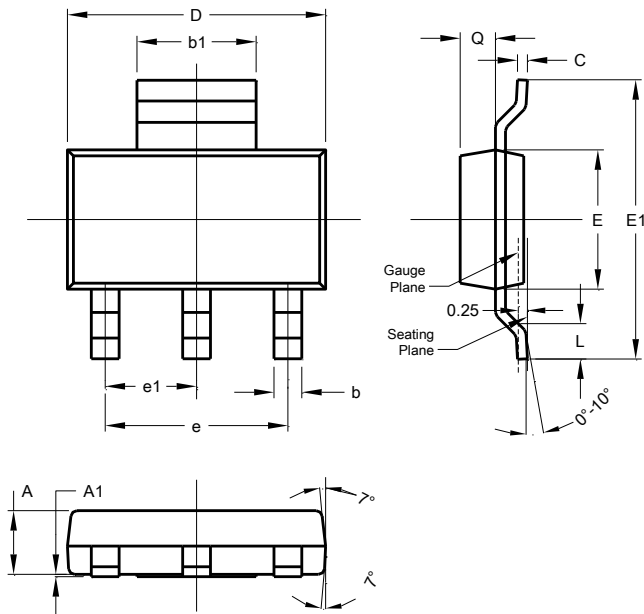
$V_{BE(on)}$ v I_C



Switching Speeds

Package Outline Dimensions

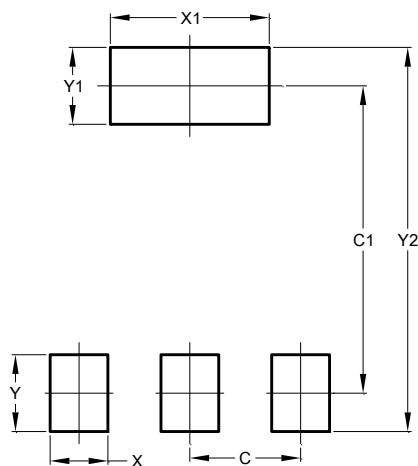
Please see <http://www.diodes.com/package-outlines.html> for the latest version.



SOT223			
Dim	Min	Max	Typ
A	1.55	1.65	1.60
A1	0.010	0.15	0.05
b	0.60	0.80	0.70
b1	2.90	3.10	3.00
C	0.20	0.30	0.25
D	6.45	6.55	6.50
E	3.45	3.55	3.50
E1	6.90	7.10	7.00
e	-	-	4.60
e1	-	-	2.30
L	0.85	1.05	0.95
Q	0.84	0.94	0.89
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Dimensions	Value (in mm)
C	2.30
C1	6.40
X	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to voltage spacing between terminals.

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2016, Diodes Incorporated

www.diodes.com